

REMARKS

Favorable reconsideration of this application, as presently amended and in light of the following discussion, is respectfully requested.

Claims 1-3 and 19 are pending in the present application, Claim 1 having been amended, Claims 4-18 having been canceled without prejudice or disclaimer, and Claims 19 having been added. Support for the amendment to Claim 1 and new Claim 19 is found, for example, in Applicants' Fig. 1-3 and Claim 4. Applicants respectfully submit that no new matter has been added.

In the outstanding Office Action, Claim 1 was rejected under 35 U.S.C. § 102(b) as anticipated by U.S. Publication No. 2002/0030268 to Sakamoto et al. (hereafter "the '268 publication"); Claims 1-3 were rejected under 35 U.S.C. § 103(a) as unpatentable over Applicant Admitted Art¹ in view of Cubero Pitel (U.S. Patent Publication No. 2005/0145595); and Claim 4 was rejected under 35 U.S.C. § 103(a) as unpatentable over Applicant Admitted Art in view of Cubero Pitel, and further in view of Ellis et al. (U.S. Patent No. 6,233,817, hereinafter Ellis).

Applicants thank the Examiners for the courtesy of an interview extended to Applicants' representative on April 1, 2009. During the interview, differences between the present invention and the applied art, and the rejections noted in the outstanding Office Action were discussed. The Examiners stated that the rejection under 35 U.S.C. §102 is withdrawn. No agreement as to allowability was reached pending the Examiner's further review when a response is filed. Arguments presented during the interview are reiterated below.

¹ It is noted that the present specification refers to Figs. 1-1 and 1-2 as "conventional" or "background art." There is no admission that Figs. 1-1 and 1-2 are prior art.

Claim 1 is amended to recite features similar to those of Claim 4. Applicants respectfully submit that amended Claim 1 patentably distinguishes over Applicant Admitted Art, Cubero Pitel, and Ellis, taken alone or in proper combination. The cited art does not disclose or suggest the claimed

a low capacitance portion embedded in the metal plate under the first insulating layer, the low capacitance portion embedded in the second insulating layer such that a sidewall of the low capacitance portion is vertically planar with a sidewall of the second insulating layer, and the low capacitance portion disposed under the another circuit pad of the plurality of the circuit pads on which the control semiconductor is mounted.

Page 5 of the outstanding Office Action acknowledges that Applicant Admitted Art and Cubero Pitel do not disclose a second insulating layer and that the low capacitance portion is embedded in the second insulating layer. The Office Action relies upon Ellis to cure the deficiencies in Applicant Admitted Art and Cubero Pitel.

The hybrid circuit structure in Fig. 2 of Ellis includes layers 120 and 122, which the Office Action appears to equate to first and second insulating layers. However, there is no low capacitance portion embedded in either of layers 120 or 122 of Ellis.

The Office Action relies upon Cubero Pitel to describe a low capacitance portion. However, filler 7, which the Office equates to the claimed “low capacitance portion” is not embedded within a second insulating layer.

Moreover, the combination of Ellis and Cubero Pitel does not disclose or suggest that filler 7 of Cubero Pitel should be embedded within layers 120 or 122 of Ellis.

Moreover, the feature of “the low capacitance portion embedded within the second insulating layer” is missing from Applicant Admitted Art.

Thus, a person of ordinary skill in the art could not properly combine Cubero Pitel, Ellis, and Applicant Admitted Art to arrive at the invention defined by Claim 1 because no

cited art discloses or suggests at least the claimed “the low capacitance portion embedded within the second insulating layer such that a sidewall of the low capacitance portion is vertically planar with a sidewall of the second insulating layer.”

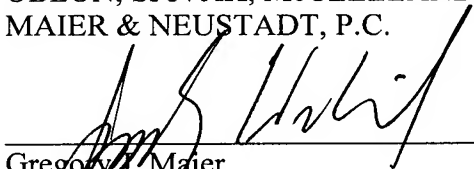
In view of the above-noted distinctions, Applicants respectfully submit that amended Claim 1 (and any claims dependent thereon) patentably distinguish over Cubero Pitel, Ellis, and Applicant Admitted Art, taken alone or in proper combination.

Moreover, new Claim 19 further patentably distinguishes over the cited art. The cited art does not disclose or suggest that the low capacitance portion is disposed directly under the another circuit pad.

Consequently, in light of the above discussion and in view of the present amendment, the present application is believed to be in condition for allowance. An early and favorable action to that effect is respectfully requested.

Respectfully submitted,

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